CC3501 weekly report example

**Group number:** 1 **Team members:** Bronson, Eric, Laurance  
**Week number:** 6

**Progress this week:** [Identify at least one small task per person that you completed this week, and have it reviewed by someone else on your team. Write 1-2 sentences about each task. For evidence of hardware design, attach a PDF file exported from Altium. For evidence of software, invite the Github user “bronsonp” to view your repository and refer to commits you have made.]

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| **Task** | **Who did it?** | **What were the outcomes?** | **Who did the peer review?** | **What did you learn?** |
| Design power delivery circuit | Bronson | Chose voltage regulator and drew schematic in Altium | Laurance | Capacitor size was not correct according to the datasheet. This was fixed. |
| Design accelerometer circuit | Eric | Reviewed available parts, chose the LIS3DH accelerometer, drew schematic in Altium | Bronson | The schematic matches the datasheet, all pins are correct. No issues identified. |
| Develop software to drive LEDs | Laurance | Created a list of functions in a new file “leds.h” to enable the rest of the software to easily control this part of the hardware. The functions have not been written yet (only their definitions are in the header file so far). | Eric | Suggested to rename some of the functions to make it more consistent with the rest of the software project. Added some more comments to the code to make it clearer. |

**Overall project tracking:** [fill this in at the beginning of the project and update weekly based on actual progress]

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| **Week number** | **Milestones** |
| 4 | Confirm project topic |
| 5 | Project begins.  Hardware design: microcontroller, USB interface, |
| 6 | Hardware design: power supply, accelerometer.  Software: specify architecture for the LED device driver. |
| 7 | Hardware design: carefully review schematic. Start on PCB layout.  Software: implement and test LED device driver. |
| 8 | Hardware design: finish PCB layout and review to make sure all design rules pass. Submit draft schematic to Bronson for review. |
| 9 | Implement fixes to the PCB.  Final PCB design submitted on Friday to Ben or Joesf for manufacturing |
| LR | Software: implement command line user interface and start on accelerometer device driver. |
| 10 | Software: implement and test accelerometer device driver. |
| 11 | Design, implement and test signal processing algorithms. |
| 12 | Verify all hardware functionality, perform testing of existing software on the physical board. Polish the software. |
| 13 | Implement final bug fixes.  Write the report.  Demo day during Friday lab. |